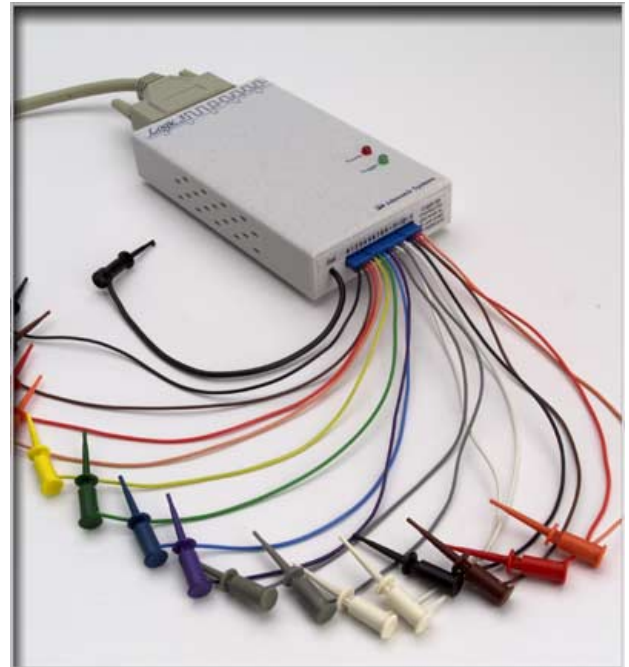




# 200MHz LOGIC ANALYZER

# TE7100

- 200MHz sampling rate
- 16 Channels
- 128K samples/channel buffer
- Flexible trigger options
- Variable input threshold
- Variable pre-/post-trigger buffer
- External clock input
- Digital logger
- Connects to PC printer port, ISA
- PCI or PXI Bus



### Capability To Go A Long Way

With the very flexible trigger capabilities of the TE7100 you can easily capture the exact data you want to view. Edge triggering may be set for a rising edge, falling edge or change-of-state. Pattern (level) conditions are set by specifying a "1", "0" or "don't care" on all channels. Combinations such as Edge OR Pattern, Edge AND Pattern, Edge THEN Edge, etc, are available. Triggering may be set for a pattern duration shorter (glitch capture) or longer, than a specified period. An "extended capture time" function even allows the final data capture to be postponed by an accurate time after trigger detection.

During continuous capture the screen may be updated at regular intervals or when a trigger condition is detected.

The large memory buffer allows much longer capture periods at higher sampling rates.

For slow changing signals the TE7100 may be used as a digital logger.

